|  |  |  |  |
| --- | --- | --- | --- |
| Type \ PHY | Physical Addresses | Size | Function |
| Code (ROM) | 0x0000\_0000 | 1 MB (1 048 576) | Instruction Memory |
| 0x000F\_FFFF |
| Code (RAM) | 0x1000\_0000 | 448 KB () | Inter SRAM: BD SRAM and Buffer SRAM |
| 0x1006\_FFFF |
|  | 0x1FFF\_0000 | 64 KB () | TCM (Tightly-Coupled Memory) SRAM |
| 0x1FFF\_FFFF |
| SRAM | 0x3000\_0000 | 2 MB () | SDR SDRAM |
| 0x301F\_FFFF |
| Peripheral | 0x4000\_0000 | 4 KB () | SYS |
| 0x4000\_0FFF |
|  | 0x4000\_1000 | 2 KB () | GPIO |
|  | 0x4000\_17FF |
|  | 0x4000\_1800 |  | Reserved (???) |
|  | 0x4000\_1FFF |
|  | 0x4000\_2000 | 4 KB () | Timer |
|  | 0x4000\_2FFF |
|  | 0x4000\_3000 | 1 KB () | UART Log |
|  | 0x4000\_33FF |
|  | 0x4000\_3400 | 1 KB () | I2C 2 |
|  | 0x4000\_37FF |
|  | 0x4000\_3800 | 1 KB () | I2C 3 |
|  | 0x4000\_3BFF |
|  | 0x4000\_3C00 |  | Reserved (???) |
|  | 0x4000\_4FFF |
|  | 0x4000\_5000 | 4 KB () | SDR SDRAM Controller |
|  | 0x4000\_5FFF |
|  | 0x4000\_6000 | 4 KB () | SPI FLASH Controller |
|  | 0x4000\_6FFF |
|  | 0x4000\_7000 |  | Reserved (???) |
|  | 0x4000\_FFFF |
|  | 0x4001\_0000 | 4 KB () | ADC |
|  | 0x4001\_0FFF |
|  | 0x4001\_1000 | 4 KB () | DAC |
|  | 0x4001\_1FFF |
|  | 0x4004\_0000 | 1 KB () | UART 0 |
|  | 0x4004\_03FF |
|  | 0x4004\_0400 |  | Reserved (???) |
|  | 0x4004\_07FF |
|  | 0x4004\_0800 | 1 KB () | UART 2 |
|  | 0x4004\_0BFF |
|  | 0x4004\_0C00 |  | Reserved (???) |
|  | 0x4004\_1FFF |
|  | 0x4004\_2000 | 1 KB () | SPI 0 |
|  | 0x4004\_23FF |
|  | 0x4004\_2400 | 1 KB () | SPI 1 |
|  | 0x4004\_27FF |
|  | 0x4004\_2800 | 1 KB () | Reserved (???) |
|  | 0x4004\_2BFF |
|  | 0x4004\_2C00 |  | Reserved (???) |
|  | 0x4004\_3FFF |
|  | 0x4004\_4000 | 1 KB () | I2C 0 |
|  | 0x4004\_43FF |
|  | 0x4004\_4400 |  | I2C 1 |
|  | 0x4004\_47FF |
|  | 0x4004\_4800 |  | Reserved (???) |
|  | 0x4004\_FFFF |
|  | 0x4005\_0000 | 16 KB () | SDIO Device / GMAC |
|  | 0x4005\_3FFF |
|  | 0x4005\_4000 |  | Reserved (???) |
|  | 0x4005\_7FFF |
|  | 0x4005\_8000 | 16 KB () | SDIO Host |
|  | 0x4005\_BFFF |
|  | 0x4005\_C000 |  | Reserved (???) |
|  | 0x4005\_FFFF |
|  | 0x4006\_0000 | 2 KB () | GDMA0 |
|  | 0x4006\_07FF |
|  | 0x4006\_0800 | 2 KB () | Reserved (for other DMA) |
|  | 0x4006\_0FFF |
|  | 0x4006\_1000 | 2 KB () | GDMA1 |
|  | 0x4006\_17FF |
|  | 0x4006\_1800 | 2 KB () | Reserved (for other DMA) |
|  | 0x4006\_1FFF |
|  | 0x4006\_2000 | 1 KB () | I2S 0 |
|  | 0x4006\_23FF |
|  | 0x4006\_2400 | 3 KB () | Reserved (???) |
|  | 0x4006\_2FFF |
|  | 0x4006\_3000 | 1 KB () | I2S 1 |
|  | 0x4006\_33FF |
|  | 0x4006\_3400 | 3 KB () | Reserved (???) |
|  | 0x4006\_3FFF |
|  | 0x4006\_4000 | 1 KB () | PCM 0 |
|  | 0x4006\_43FF |
|  | 0x4006\_4400 | 3 KB () | Reserved (???) |
|  | 0x4006\_4FFF |
|  | 0x4006\_5000 | 1 KB () | PCM 1 |
|  | 0x4006\_53FF |
|  | 0x4007\_0000 | 16 KB () | Security Engine |
|  | 0x4007\_3FFF |
|  | 0x4007\_4000 | 48 KB () | Reserved (???) |
|  | 0x4007\_FFFF |
|  | 0x4008\_0000 | 256 KB () | Wi-Fi, Tx\Rx FiFo Direct Map |
|  | 0x400B\_FFFF |
|  | 0x400C\_0000 | 256 KB () | USB OTG, Data FiFo Direct Map |
|  | 0x400F\_FFFF |
|  | 0x4030\_FFFF | 1 MB () | Reserved (???) |
|  | 0x403F\_FFFF |
|  | 0x9800\_0000 | 64 MB () | External FLASH |
|  | 0x9BFF\_FFFF |